



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in light of NEP-2020
Diploma in Electronics

SEMESTER- II (2024-2027)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	DTMA201N	Applied Mathematics II	3	0	0	3	60	20	20	0	0	100
2	DTCH101	Engineering Chemistry	2	1	2	4	60	20	20	30	20	150
3	DTEI301N	Measurement and Instrumentation	3	0	0	3	60	20	20	0	0	100
4	DTEE202	Basic Electronics Engineering	2	1	2	4	60	20	20	30	20	150
5	DTHU101	Communication Skills	0	0	4	2	0	0	0	30	20	50
6	DTET201	Computer Basics	0	0	2	1	0	0	0	30	20	50
TOTAL			10	2	10	17	240	80	80	120	80	600

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Controller of Examinations
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore